

EAST Search History

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L1	298	soft adj programming	US-PGPUB; USPAT	OR	ON	2006/06/07 14:10
L2	37745	maximum adj3 current	US-PGPUB; USPAT	OR	ON	2006/06/07 14:10
L3	30	1 and 2	US-PGPUB; USPAT	OR	ON	2006/06/07 14:10
L4	6	la-malfa-antonino.in.	US-PGPUB; USPAT	OR	ON	2006/06/07 14:10
L5	6	messina-marco.in.	US-PGPUB; USPAT	OR	ON	2006/06/07 14:11

Day : Wednesday

Date: 6/7/2006

Time: 14:26:49

PALM INTRANET**Inventor Name Search Result**

Your Search was:

Last Name = LA MALFA

First Name = ANTONINO

Application#	Patent#	Status	Date Filed	Title	Inventor Name
<u>10623474</u>		Not Issued	71	07/18/2003	Automatic decoding method for mapping and selecting a non-volatile memory device having a LPC serial communication interface in the available addressing area on motherboards
<u>10686552</u>	<u>6922362</u>	150	10/15/2003	STRUCTURE FOR UPDATING A BLOCK OF MEMORY CELLS IN A FLASH MEMORY DEVICE WITH ERASE AND PROGRAM OPERATION REDUCTION	LA MALFA, ANTONINO
<u>10748447</u>	<u>6885584</u>	150	12/30/2003	FAST PAGE PROGRAMMING ARCHITECTURE AND METHOD IN A NON-VOLATILE MEMORY DEVICE WITH AN SPI INTERFACE	LA MALFA, ANTONINO
<u>10779856</u>		Not Issued	71	02/17/2004	Method for soft-programming an electrically erasable nonvolatile memory device, and an electrically erasable nonvolatile memory device implementing the soft-programming method
<u>11337030</u>		Not Issued	30	01/20/2006	Circuit for generating an internal enabling signal for an output buffer of a memory

Inventor Search Completed: No Records to Display.

Search Another: Inventor**Last Name**

la malfa

First Name

antonino

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Day : Wednesday

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PALM INTRANET**Inventor Name Search Result**

Your Search was:

Last Name = MESSINA

First Name = MARCO

Application#	Patent#	Status	Date Filed	Title	Inventor Name
<u>09196913</u>	6054932	150	11/20/1998	LED TRAFFIC LIGHT AND METHOD OF MANUFACTURE AND USE THEREOF	MESSINA, MARCO
<u>10199965</u>	Not Issued	71	07/18/2002	Hybrid architecture for realizing a random numbers generator	MESSINA, MARCO
<u>10270020</u>	Not Issued	93	10/11/2002	RANDOM BIT SEQUENCE GENERATOR	MESSINA, MARCO
<u>10447293</u>	6785174	150	05/28/2003	TESTING METHOD AND DEVICE FOR NON-VOLATILE MEMORIES HAVING A LPC (LOW PIN COUNT) COMMUNICATION SERIAL INTERFACE	MESSINA, MARCO
<u>10779856</u>	Not Issued	71	02/17/2004	Method for soft-programming an electrically erasable nonvolatile memory device, and an electrically erasable nonvolatile memory device implementing the soft-programming method	MESSINA, MARCO
<u>11336411</u>	Not Issued	19	01/19/2006	Method and architecture for restricting access to a memory device	MESSINA, MARCO
<u>11337030</u>	Not Issued	30	01/20/2006	Circuit for generating an internal enabling signal for an output buffer of a memory	MESSINA, MARCO
<u>11337085</u>	Not Issued	20	01/19/2006	Enhanced security memory access method and architecture	MESSINA, MARCO
<u>60307002</u>	Not Issued	159	07/20/2001	Hybrid architecture for realizing a random numbers generator	MESSINA, MARCO
<u>60781355</u>	Not Issued	20	03/09/2006	Web navigation interface for blind users	MESSINA, MARCO
<u>60781506</u>	Not Issued	20	03/09/2006	Voice-based telecommunication language translator	MESSINA, MARCO

Inventor Search Completed: No Records to Display.

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